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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: Sang Sic Yoon

Confirmation No.: 7314

Serial No.: 10/608,569

Art Unit: 2816

Filed: June 30, 2003

Examiner: My Trang TON

For: DIGITAL CONTROL LOGIC CIRCUIT HAVING A CHARACTERISTIC OF TIME  
HYSTERESIS

AMENDMENT AND REPLY UNDER 37 C.F.R. § 1.111

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant herein responds to the Office Action mailed May 20, 2004 (Paper No./Mail Date 20040513), for the above-captioned application. While Applicant believes that no fee is required, the Commissioner is authorized to charge any deficiency to Deposit Account No. 08-1641.

Please amend the Application as set forth in the Amendments below.